



US/N 10/731,393

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Chaiyuth Chansungsan et al.	Examiner:	Tuan T. Lam
Serial No.:	10/731,393	Group Art Unit:	2816
Filed:	December 09, 2003	Docket:	884.A83US1
Title:	POWER SUPPLIES NOISE DETECTOR FOR INTEGRATED CIRCUITS		
Assignee:	Intel Corporation	Customer Number:	21186

INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), please charge Deposit Account No. 19-0743 in the amount of \$180.00 as set forth in 37 C.F.R. §1.17(p).

Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

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Dkt: 884.A83US1 (INTEL)

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

CHAIYUTH CHANSUNGSAN ET AL.

By their Representatives,

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Date June 20, 2005

By

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 20th day of June, 2005.

Daniel J. Kluth

Name

Signature

Daniel J. Kluth

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	10/731,393
	Filing Date	December 9, 2003
	First Named Inventor	Chansungsan, Chaiyuth
	Group Art Unit	2816
	Examiner Name	Lam, Tuan
Sheet 1 of 1	Attorney Docket No: 884.A83US1	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		BAZES, MEL , "Two Novel Fully Complementary Self-Biased CMOS Differential Amplifiers", <u>IEEE Journal of Solid-State Circuits</u> , 26(2), (February 1991),165-168	
		BUCK, A E., et al., "A CMOS bandgap reference without resistors", <u>IEEE Journal of Solid-State Circuits</u> , 37(1), (January 2002),81-83	

EXAMINER

DATE CONSIDERED